

**WEST**

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L6 and ((RAM or (volatile adj2 (memory or storage))) near8 (large or size or capacity or dimension or proportion or amount) near8 ((boot\$3 or initiali\$8) near4 code))	7

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result set

*DB=USPT; PLUR=YES; OP=ADJ*

<u>L7</u>	L6 and ((RAM or (volatile adj2 (memory or storage))) near8 (large or size or capacity or dimension or proportion or amount) near8 ((boot\$3 or initiali\$8) near4 code))	7	<u>L7</u>
<u>L6</u>	(RAM or (volatile adj2 (memory or storage))) near8 ((boot\$3 or initiali\$8) near4 code)	278	<u>L6</u>
<u>L5</u>	L4 and (boot\$3 or start\$3 or startup or initiali\$8)	2	<u>L5</u>
<u>L4</u>	(6201739 or 6434695).pn.	2	<u>L4</u>
<u>L3</u>	L2 and l1	58	<u>L3</u>
<u>L2</u>	(logic or control\$4) with code with (RAM or (volatile adj2 memory))	3599	<u>L2</u>
<u>L1</u>	flash with code with (processor or cpu)	469	<u>L1</u>

END OF SEARCH HISTORY

**Set Name Query**

side by side

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result set

*DB=USPT; PLUR=YES; OP=ADJ*

<u>L22</u>	L21 and (flash near8 (single adj2 (die or chip)))	5	<u>L22</u>
<u>L21</u>	L19 and (flash near8 (SRAM or DRAM or volatile))	51	<u>L21</u>
<u>L20</u>	L19 and (flash near8 (single adj2 (die or chip)))	6	<u>L20</u>
<u>L19</u>	l14 and l11	92	<u>L19</u>
<u>L18</u>	l14 and ((flash or ROM or (non adj volatile)) near8 (single adj2 (die or chip)))	84	<u>L18</u>
<u>L17</u>	L16 and (capacitor near8 stor\$4 near4 power)	2	<u>L17</u>
<u>L16</u>	L14 and l7	262	<u>L16</u>
<u>L15</u>	L14 and l9	19	<u>L15</u>
<u>L14</u>	((711/\$)!.CCLS.)	14262	<u>L14</u>
<u>L13</u>	L11 same (capacitor near8 (stor\$4 or hold\$3) near4 power)	36	<u>L13</u>
<u>L12</u>	L11 same (capacitor near8 stor\$4 near4 power)	36	<u>L12</u>
<u>L11</u>	flash adj3 unit	3818	<u>L11</u>
<u>L10</u>	L9 and (capacitor near8 stor\$4 near4 power)	25	<u>L10</u>
<u>L9</u>	L7 same (power near4 (shut\$4 or available or unavailable))	178	<u>L9</u>
<u>L8</u>	L7 and (power near4 (shut\$4 or available or unavailable))	656	<u>L8</u>
<u>L7</u>	((flash or ROM or (non adj volatile)) near8 ((power or capacitor) near8 stor\$4))	4548	<u>L7</u>
<u>L6</u>	l1 and ((flash or ROM or (non adj volatile)) near8 ((power or capacitor) near8 stor\$4))	1	<u>L6</u>
<u>L5</u>	l1 and ((flash or ROM or (non adj volatile)) near8 (size or capacity or amount))	4	<u>L5</u>
<u>L4</u>	L3 and (size or capacity or amount)	3	<u>L4</u>
<u>L3</u>	l2 and code	3	<u>L3</u>
<u>L2</u>	L1 and (boot\$3 or power\$3 or capacitor)	3	<u>L2</u>
<u>L1</u>	(6434695 or 6201739 or 6523101 or 6385688).pn.	4	<u>L1</u>

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